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## **Implementing an FFT with the HERON-FPGA Family**

Rev 1.0 R.Williams 11-05-05

The 'General Examples' provided by HUNT ENGINEERING include an example of how to implement an FFT with FPGA.

The example can be adapted to work on any of the FPGA modules, but in particular it has been provided as a worked example for the HERON-IO2v2.

The worked example for the HERON-IO2v2 is located under the CD directory 'fpga\io2v2\fft\_example'. An example project is supplied for use with ISE along with ready to load bit-streams for the IO2v2.